



Dkt. 2271/60617

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of : Yoshinori UEDA

Serial No. : 09/431,593

Group Art Unit: 2815

Date Filed : November 1, 1999

Examiner: M. E. Warren

For : SEMICONDUCTOR DEVICE HAVING AN
INTEGRAL RESISTANCE ELEMENT

1185 Avenue of the Americas
New York, N.Y. 10036

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

Sir:

In response to the first Office Action dated August 1, 2001, please amend the above-identified application as follows:

In the Claims:

Please amend claims 1 and 7 as follows:

- Sub B1
9/1
Cmt
1. (Amended) A semiconductor device, comprising:
- a Si substrate; and
 - a resistance element formed on said Si substrate,

I hereby certify that this paper is being deposited this date with the
U.S. Postal Service as first class mail addressed to the Assistant
Commissioner for Patents, Washington, D.C. 20231.

Richard F. Jaworski
Reg. No. 33.515

Date

Oct 26, 2001

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V. Varnae